

A physical channel-potential and drain-current model for asymmetric dual-gate a-IGZO TFTs

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Dear editor,

A dual-gate (DG) structure demonstrably has improved the device performance and stability of amorphous InGaZnO (a-IGZO) thin-film transistors (TFTs), making circuits faster, less power-consuming, and more stable [1–3]. Physical device modeling has thus generated much interest in circuit design. Ref. [4] derived simple equations describing the DC performance of DG a-IGZO TFTs, empirically based on DG MOSFET models. Ref. [5] proposed an analytical potential model for a-IGZO TFTs with a synchronized symmetric DG (SDG) structure. In previous work, we modeled the threshold voltage and drain current of SDG a-IGZO TFTs by considering the physical properties of active films [6]. There are, nonetheless, several notable challenges. First, perfectly symmetric DG configurations are difficult to fabricate, and also that the two gates are often biased separately. Second, the charge-sheet approximation becomes invalid in DG devices showing bulk accumulation. Moreover, conventional DG MOSFET models do not apply to DG a-IGZO TFTs, where basic semiconductor equations with the sub-gap density of states (DOS) are more difficult to solve.

Given that asymmetric DG (ADG) configurations are more commonly used than SDG, we derived accurate solutions of Poisson's equation and expressions for the physical drain current in ADG a-IGZO TFTs.

Potential model. Band diagrams of the device are shown in Figure 1. Within the gradual chan-

nel approximation, Poisson's equation is written $\nabla^2\varphi = -\frac{dE}{dx}$. By considering trap-limited conduction (TLC) in a-IGZO, integrating Poisson's equation from 0 to x yields the electric field

$$-E = \frac{d\varphi}{dx} = \pm \left\{ \alpha + \frac{2q}{\varepsilon_s} \left[N_t \varphi_t \exp\left(\frac{\varphi - V_{CH}}{\varphi_t}\right) + N_f \varphi_{th} \exp\left(\frac{\varphi - V_{CH}}{\varphi_{th}}\right) \right] \right\}^{1/2}, \quad (1)$$

where φ and V_{CH} are, respectively, the electrostatic potential and quasi-Fermi potential, ε_s is the permittivity of a-IGZO, $N_f = N_C \exp(\frac{-\varphi_{F0}}{\varphi_t})$, $N_t = N_{TA} \frac{\pi T/T_t}{\sin(\pi T/T_t)} \exp(\frac{-\varphi_{F0}}{\varphi_t})$, φ_{F0} is the potential difference between the conduction-band edge and the initial position of the Fermi level, N_C is the effective density of states in the conduction band, N_{TA} is the total number of trap states per unit volume, $\varphi_{th} = \frac{kT}{q}$, $\varphi_t = \frac{kT_t}{q}$, T is the room temperature, T_t is the characteristic temperature of trap states, and $\alpha = (\frac{d\varphi}{dx})^2|_{x=0} - \frac{2q}{\varepsilon_s} [N_t \varphi_t \exp(\frac{\varphi_B - V_{CH}}{\varphi_t}) + N_f \varphi_{th} \exp(\frac{\varphi_B - V_{CH}}{\varphi_{th}})]$, in which φ_B is the potential at the bottom surface. The other symbols have their usual meanings.

Boundary conditions at the bottom and top surfaces of the active layer can be formulated from Gauss's law as

$$\begin{aligned} -\varepsilon_s (d\varphi/dx)|_{x=0} &= C_{oxB}(V_{GBf} - \varphi_B), \\ \varepsilon_s (d\varphi/dx)|_{x=t_s} &= C_{oxT}(V_{GTf} - \varphi_T), \end{aligned} \quad (2)$$

where $C_{oxB(oxT)}$ denotes the bottom-gate (BG) or top-gate (TG) capacitance per unit area,

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$V_{\text{GBf(GTf)}}$ is the BG or TG bias with the flat-band voltage, φ_{T} is the top-surface potential, and t_{s} is the channel layer thickness.

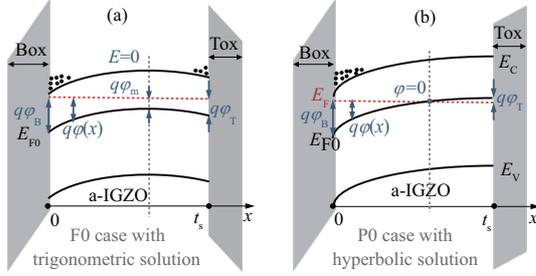


Figure 1 (Color online) Band diagrams in the vertical (x -) direction to the channel for (a) the F0 case and (b) the P0 case.

The integral of (1) cannot be calculated directly. The sub-gap DOS and the occurrence of TLC in a-IGZO suggest that the trapped and free charges dominate conduction for low and high gate voltages, respectively. Thus, an effective characteristic temperature T_{eff} , or its equivalent voltage $\varphi_{\text{eff}} = \frac{kT_{\text{eff}}}{q}$, is introduced. Eq.(1) is rewritten as

$$-E = \frac{d\varphi}{dx} = \pm \sqrt{\alpha_{\text{eff}} + (F_t + F_f) \exp\left(\frac{\varphi - V_{\text{CH}}}{\varphi_{\text{eff}}}\right)}, \quad (3)$$

where $\alpha_{\text{eff}} = \left(\frac{C_{\text{oxB}}}{\varepsilon_{\text{s}}}\right)^2 (V_{\text{GBf}} - \varphi_{\text{B}})^2 - (F_t + F_f) \exp\left(\frac{\varphi_{\text{B}} - V_{\text{CH}}}{\varphi_{\text{eff}}}\right)$, $F_t = \frac{2qN_t\varphi_t}{\varepsilon_{\text{s}}}$, $F_f = \frac{2qN_f\varphi_{\text{th}}}{\varepsilon_{\text{s}}}$. As the gate voltage increases, φ_{eff} gradually changes from being approximately equal to φ_t to approaching φ_{th} . Thus, Eq. (3) is equivalent to (1). The method used for obtaining φ_{eff} will be described below. We here focus on solving (3), first by taking $V_{\text{GBf}} > V_{\text{GTf}}$ as an example. By integrating the right-hand side of (3) from 0 to x , we obtain

$$\varphi(x) = V_{\text{CH}} - 2\varphi_{\text{eff}} \ln \left\{ \sqrt{\frac{F_t + F_f}{(-) - \alpha_{\text{eff}}}} \cdot \sin(\text{h}) \left[\frac{\sqrt{(-) - \alpha_{\text{eff}}}}{2\varphi_{\text{eff}}} x + \beta_{\text{eff}}^{(*)} \right] \right\}, \quad (4)$$

where $\beta_{\text{eff}}^{(*)} = \arcsin(\text{h}) \left[\sqrt{\frac{(-) - \alpha_{\text{eff}}}{F_t + F_f}} \exp\left(-\frac{\varphi_{\text{B}} - V_{\text{CH}}}{2\varphi_{\text{eff}}}\right) \right]$. Combining (2) with (4), we obtain an equation for calculating φ_{B} :

$$\frac{C_{\text{oxT}}}{\varepsilon_{\text{s}}} (V_{\text{GTf}} - \varphi_{\text{T}}) = -\sqrt{(-) - \alpha_{\text{eff}}} \cdot \cot(\text{h}) \left[\frac{\sqrt{(-) - \alpha_{\text{eff}}}}{2\varphi_{\text{eff}}} t_{\text{s}} + \beta_{\text{eff}}^{(*)} \right], \quad (5)$$

where φ_{T} is expressed by (4) at $x = t_{\text{s}}$. The related parentheses in (4) and (5) are ignored when $\alpha_{\text{eff}} < 0$, corresponding to the trigonometric solution or the zero-field (F0) case (Figure 1(a)) [7]. These parentheses will be kept if $\alpha_{\text{eff}} > 0$, indicating the hyperbolic solution or the zero-potential (P0) case (Figure 1(b)). For devices at given DG biases, the form of the solution must be determined first. By applying $\alpha_{\text{eff}} = 0$, we obtain

$$\varphi_{\text{Bcr}} = V_{\text{GBf}} - 2\varphi_{\text{eff}} W_0 \left[\sqrt{\frac{\varepsilon_{\text{s}}^2 (F_t + F_f)}{4\varphi_{\text{eff}}^2 C_{\text{oxB}}^2}} \cdot \exp\left(\frac{V_{\text{GBf}} - V_{\text{CH}}}{2\varphi_{\text{eff}}}\right) \right], \quad (6)$$

which is the critical bottom-surface potential when the two forms of the solution in (4) and (5) are equivalent, and W_0 is the Lambert W function. Substituting (6) into (5) with $\alpha_{\text{eff}}=0$, we obtain the critical TG voltage as

$$V_{\text{GTfcr}} = V_{\text{CH}} - 2\varphi_{\text{eff}} \ln(\Gamma_{\text{Bcr}}) - \frac{\varepsilon_{\text{s}} \sqrt{F_t + F_f}}{C_{\text{oxT}} \Gamma_{\text{Bcr}}}, \quad (7)$$

where $\Gamma_{\text{Bcr}} = \frac{t_{\text{s}} \sqrt{F_t + F_f}}{2\varphi_{\text{eff}}} + \exp\left(-\frac{\varphi_{\text{Bcr}} - V_{\text{CH}}}{2\varphi_{\text{eff}}}\right)$. If $V_{\text{GTf}} > V_{\text{GTfcr}}$, the trigonometric form is chosen, otherwise the hyperbolic form is chosen in (5) to solve for φ_{B} . Once φ_{B} is calculated, potential distributions in the active layer can be obtained accordingly using (4).

By considering the case where either only trapped or free carriers are present in (1) and (2), and applying the same procedure as in (4)–(7), the bottom-surface potential with only the trapped ($\varphi_{\text{B,t}}$) or free charge ($\varphi_{\text{B,f}}$) is obtained. These can then be combined as

$$\varphi_{\text{Bcon}} = \frac{\varphi_{\text{B,t}}}{1 + e^{(V_{\text{GBf}} - V_{\text{eq}})}} + \frac{\varphi_{\text{B,f}}}{1 + e^{(V_{\text{eq}} - V_{\text{GBf}})}}, \quad (8)$$

where V_{eq} is the V_{GBf} when $\varphi_{\text{B,t}} = \varphi_{\text{B,f}}$. The quantity φ_{eff} can thus be calculated by equating the exponential terms in (1) and (3) with φ_{Bcon} , expressed as

$$\varphi_{\text{eff}} = \Phi(\varphi_{\text{Bcon}}) = \frac{\varphi_{\text{Bcon}}}{\ln[F(\varphi_{\text{Bcon}})/(F_t + F_f)]} \Big|_{V_{\text{CH}}=0}, \quad (9)$$

where $F(\varphi_{\text{Bcon}}) = F_t \exp\left(\frac{\varphi_{\text{Bcon}}}{\varphi_t}\right) + F_f \exp\left(\frac{\varphi_{\text{Bcon}}}{\varphi_{\text{th}}}\right)$. Note that, if $V_{\text{GTf}} > V_{\text{GBf}}$, the above equations still apply when swapping the subscripts “B” and “T”. In the F0 case, the potential extremum (φ_{m}) can be obtained by setting $E = 0$ in (3), giving $\varphi_{\text{m}} = V_{\text{CH}} + \varphi_{\text{eff}} \ln\left(\frac{-\alpha_{\text{eff}}}{F_t + F_f}\right)$.

Drain current model. To derive the drain current, we first consider Pao-Sah’s double integral

$$I_{\text{DS}} = \int_0^{V_{\text{DS}}} \int_{\varphi_{\text{B}}}^{\varphi_{\text{T}}} \frac{\mu W}{L} q N_f \exp\left(\frac{\varphi - V_{\text{CH}}}{\varphi_{\text{th}}}\right) d\varphi dV_{\text{CH}}, \quad (10)$$

where μ is the carrier mobility, W and L are the channel width and length, and V_{DS} is the drain-to-source voltage. Following the method of Pierret and Shields [8], the partial derivative of (1) with respect to V_{CH} gives

$$\exp\left(\frac{\varphi - V_{CH}}{\varphi_{th}}\right) = \frac{\varphi_{th}}{F_f} \left[\frac{d\alpha}{dV_{CH}} - \frac{2E\partial E}{\partial V_{CH}} - \frac{F_t}{\varphi_t} \exp\left(\frac{\varphi - V_{CH}}{\varphi_t}\right) \right]. \quad (11)$$

An approximation of the third term in (11) yields

$$\exp\left(\frac{\varphi - V_{CH}}{\varphi_{th}}\right) = \frac{\varphi_{th}}{F_f} \left\{ \frac{d\alpha}{dV_{CH}} - \frac{2E\partial E}{\partial V_{CH}} - \frac{1}{\varphi_t} \left[\xi (F_t + F_f) \exp\left(\frac{\varphi - V_{CH}}{\theta\varphi'_{eff}}\right) - F_f \exp\left(\frac{\varphi - V_{CH}}{\varphi_{th}}\right) \right] \right\}, \quad (12)$$

where $\varphi'_{eff} = \Phi(\varphi_B)$, ξ and θ are fitting parameters. With this approach and after some mathematical manipulations (see Appendix A), the drain current becomes

$$I_{DS} = \delta A \left\{ t_s [(\alpha_L - \alpha_0) - B(\alpha'_{effL} - \alpha'_{eff0})] + D \left[\frac{C_{oxB}}{\varepsilon_s} (\varphi_{BL} - \varphi_{B0}) (2V_{GBf} - \varphi_{BL} - \varphi_{B0}) + \frac{C_{oxT}}{\varepsilon_s} (\varphi_{TL} - \varphi_{T0}) (2V_{GTf} - \varphi_{TL} - \varphi_{T0}) + 2(K_L - K_0) \right] \right\}, \quad (13)$$

where $A = \frac{\mu W}{L} \frac{qN_f\varphi_t\varphi_{th}}{F_f(\varphi_t - \varphi_{th})}$, $B = \frac{\xi\theta\varphi'_{eff}}{\varphi_t}$, $D = 1 - B$, $\alpha_{L(0)}$ is the value of α corresponding to $V_{CH} = V_{DS}$ ($V_{CH} = 0$), $\varphi_{BL(B0)}$ and $\varphi_{TL(T0)}$ are the values of φ_B and φ_T for $V_{CH} = V_{DS}$ ($V_{CH} = 0$). Note that the function δ avoids singularities once $B \geq 1$ in the deep subthreshold region. The quantities $\alpha'_{effL(eff0)}$ and $K_{L(0)}$ are intermediate variables, expressed as shown in Appendix A.

Model validation and discussion. The potential model was verified by rigorous numerical calculations, as presented in Appendix B. The verification of the proposed drain-current model is given in Appendix C, in which the transfer and output curves are well described under various DG biases. Because of the repulsion and attraction of carriers by the TG field, the transfer curve and threshold voltage show parallel shifts along the BG-voltage axis by a TG-dependent amount. This constitutes an advantage for ADG a-IGZO TFTs with TG control, stemming from the absence of a hole-accumulation layer in a-IGZO. The drain currents and potential distributions for various active-

layer thicknesses have also proven to be well characterized by this model. A higher on-state current and a steeper subthreshold slope are observed when the a-IGZO layer is thinner than the Debye length, indicating the extension of the accumulation/depletion layer throughout the a-IGZO bulk. Since no charge-sheet approximation is involved, this model applies also to devices exhibiting bulk accumulation or depletion.

Conclusion. We presented a convergent and efficient solution for the channel potential of ADG a-IGZO TFTs. An expression for the analytical and continuous drain current was then derived, providing a physical interpretation of device DC characteristics. This model provides an effective approach for reducing the complexity induced by the sub-gap DOS, while capturing the basic underlying physics. It can therefore serve as the basis for potential-based models, amenable to implementation in device and circuit simulators.

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Supporting information Appendixes A–C. The supporting information is available online at info.scichina.com and link.springer.com. The supporting materials are published as submitted, without typesetting or editing. The responsibility for scientific accuracy and content remains entirely with the authors.

References

- Kim D S, Kwon O K. A small-area and low-power scan driver using a coplanar a-IGZO thin-film transistor with a dual-gate for liquid crystal displays. *IEEE Electron Device Lett*, 2017, 38: 195–198
- Jeon C H, Um J G, Mativenga M, et al. Fast threshold voltage compensation AMOLED pixel circuit using secondary gate effect of dual gate a-IGZO TFTs. *IEEE Electron Device Lett*, 2016, 37: 1450–1453
- Baek G, Abe K, Kuo A, et al. Electrical properties and stability of dual-gate coplanar homojunction DC sputtered amorphous indium-gallium-zinc-oxide thin-film transistors and its application to AM-OLEDs. *IEEE Trans Electron Device*, 2011, 58: 4344–4353
- Baek G, Kanicki J. Modeling of current-voltage characteristics for double-gate a-IGZO TFTs and its application to AMLCDs. *J Soc Inf Display*, 2012, 20: 237–244
- Qin T, Huang S X, Liao C W, et al. Analytical channel potential model of amorphous InGaZnO thin-film transistors with synchronized symmetric dual-gate (in Chinese). *Acta Phys Sin*, 2017, 66: 097101
- Cai M X, Yao R H. A threshold voltage and drain current model for symmetric dual-gate amorphous InGaZnO thin film transistors. *Sci China Inf Sci*, 2018, 61: 022401
- Shi X J, Wong M. Analytical solutions to the one-dimensional oxide-silicon-oxide system. *IEEE Trans Electron Device*, 2003, 50: 1793–1800
- Pierret R F, Shields J A. Simplified long-channel MOS-FET theory. *Solid-State Electron*, 1983, 26: 143–147