

Efficient flush-reload cache attack on scalar multiplication based signature algorithm

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Appendix A Illustration for Figure 1(d)

Fig.1(d) illustrates the measured loading time of the monitored memory lines $addr_A$ and $addr_D$ during 100 round of monitoring. The horizontal axis is the round of monitoring and the loading time is shown on the vertical axis. The dashed line is the threshold h which is equal to the median value of L3 cache access delay and memory access delay. Loading time which is less than the threshold indicates a victim access to the monitored memory line. The operations executed by the victim during this period can be identified clearly as 'DDDADADADDAD'. In binary method, the scalar bit 0 corresponds to a 'D' operation and the scalar bit 1 corresponds to an 'AD' operation, therefore the fragment of the scalar bits can be recovered as '00111010'.

Appendix B Apply to sliding window and wNAF

The presented method is not limited to the scalar multiplication using the binary method. We also apply this attack to ECDSA using wNAF method [1] and SM2-DSA using sliding window method [2] in OpenSSL. Both of these two methods reform the representation of the scalar and process multiple scalar bits during a single iteration. Thus, the scalar multiplication can be computed faster as the total number of additions is reduced. In OpenSSL, the window size is set as 3-bit for 256-bit scalars. In this situation, our method can recover the AD sequence with an error rate as low as 0.15%. In 73% of the signatures, the sequence of operations are fully recovered. The rest work to recover the private key need to use the lattice techniques [3] [4]. With lattice attack, $\lceil 256/l \rceil$ signature samples are required to recover a 256-bit private key when last l bits of the scalar is known. The sequence of operations which end with 'ADD' or 'DDD' can confirm a scalar ending with '100' or '000'. As the probability of this bit pattern is expected as 2^{-2} , the adversary need about $\lceil 256/3 \rceil / (2^{-2}) = 344$ signatures to recover the whole private key.

Appendix C Comparison with other monitored memory

In this letter, we select the monitored memory lines that contain four *call* instructions, resulting in 0.96 error bits in each 256-bit scalar. We also conduct the same experiment but select the monitored memory lines that contain no *call* instructions, resulting in more than 25 error bits in each 256-bit scalar. This result shows that the presented method effective.

References

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